

FIG. 1

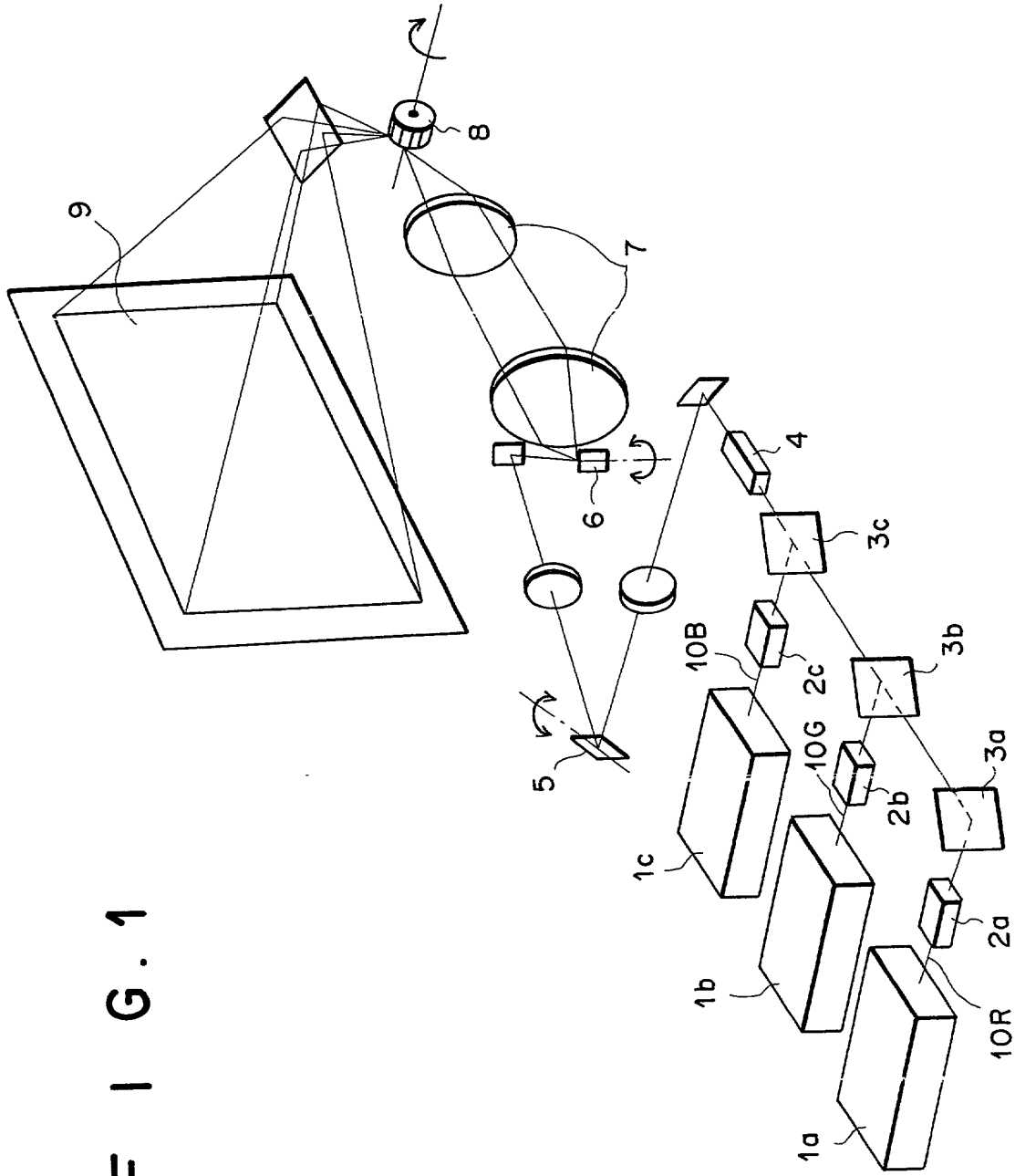
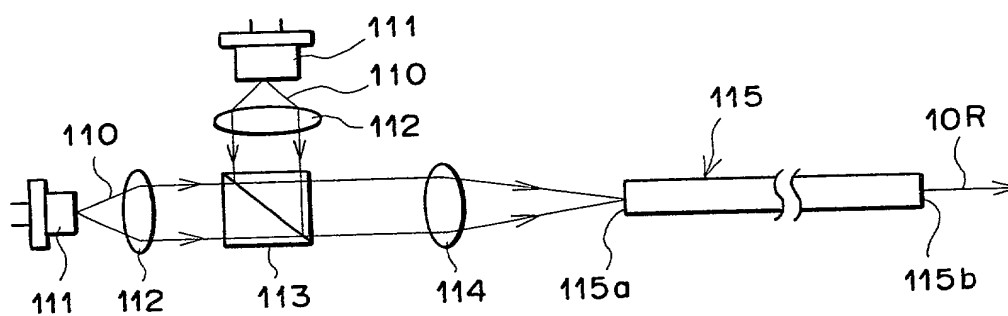
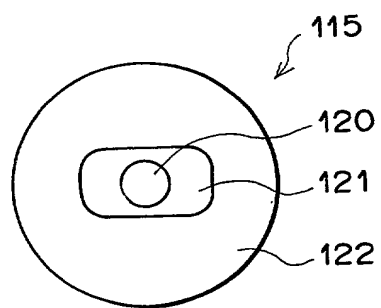


FIG. 2

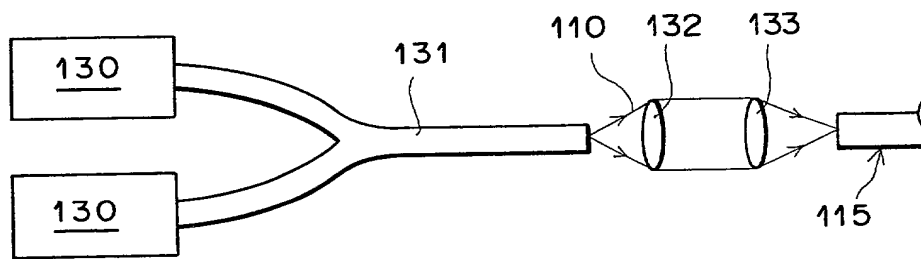
1a



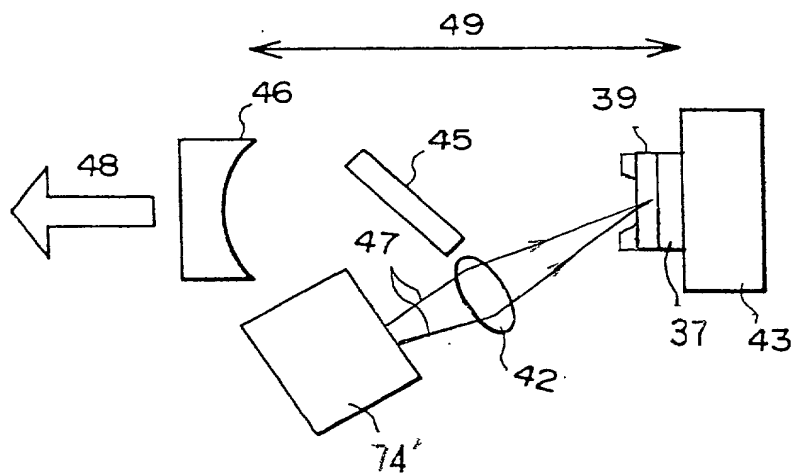
F I G. 3



F I G . 4



F I G . 5



F I G . 6

74
↓

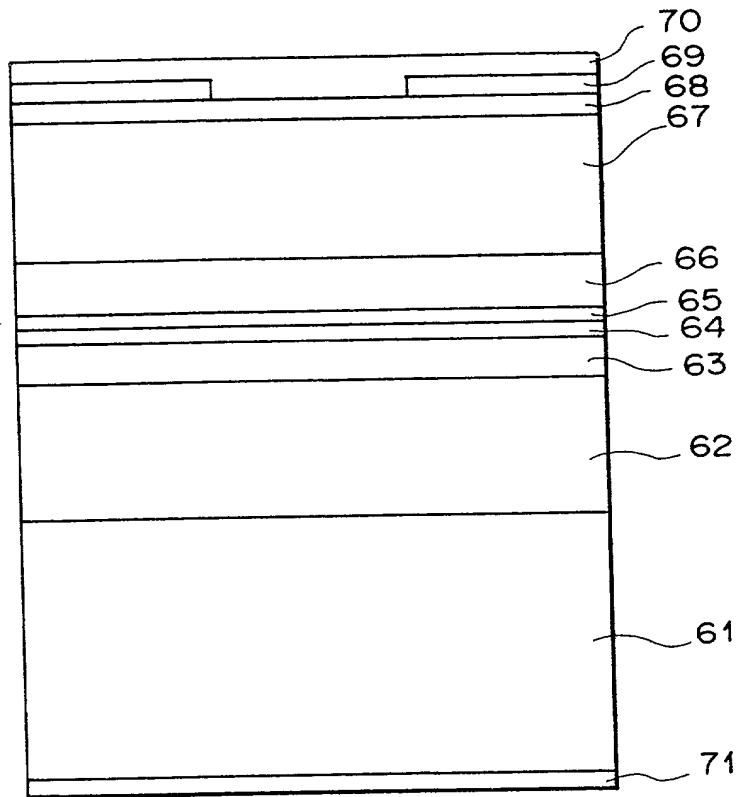


FIG. 7

39

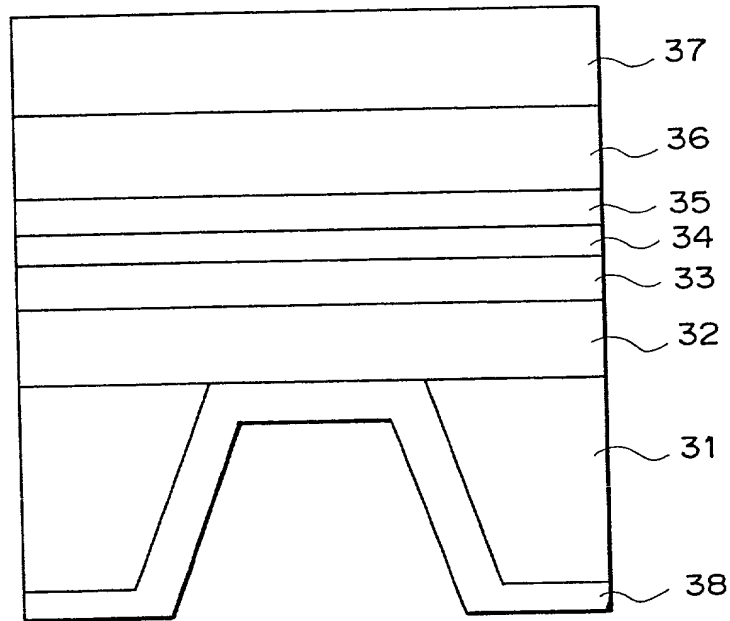


FIG. 8A is a schematic diagram of a device 100 in a first configuration. The device 100 includes a housing 101, a display 102, a camera 103, a sensor 104, a processor 105, and a memory 106. The display 102 is positioned at the top of the housing 101, and the camera 103 is positioned below the display 102. The sensor 104 is positioned on the side of the housing 101, and the processor 105 and memory 106 are positioned at the bottom of the housing 101. The device 100 is shown in a first configuration where the display 102 is in a closed position, and the camera 103 is not visible. A dimension line 109 indicates the height of the device 100. A downward arrow 108 indicates the direction of movement of the device 100.

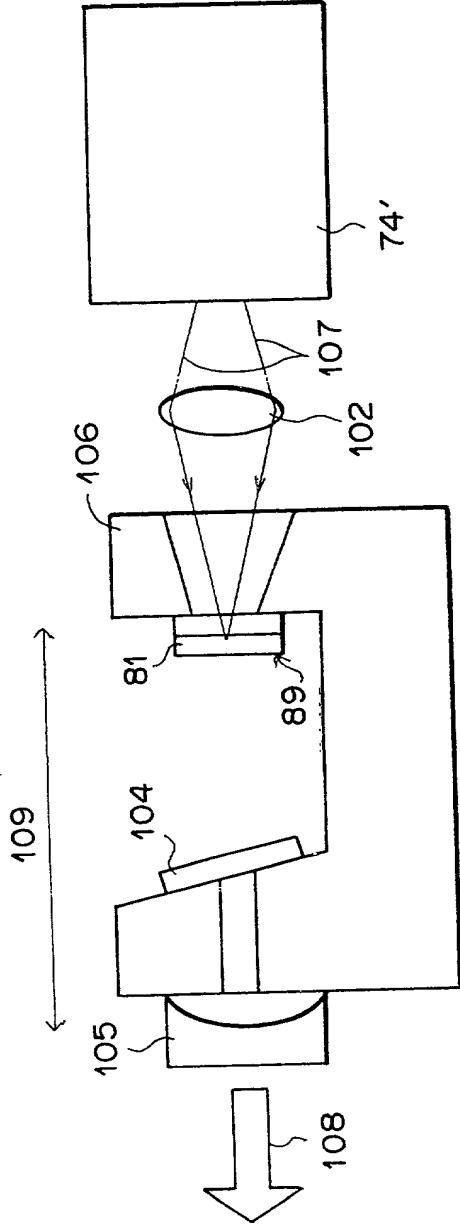


FIG. 8A

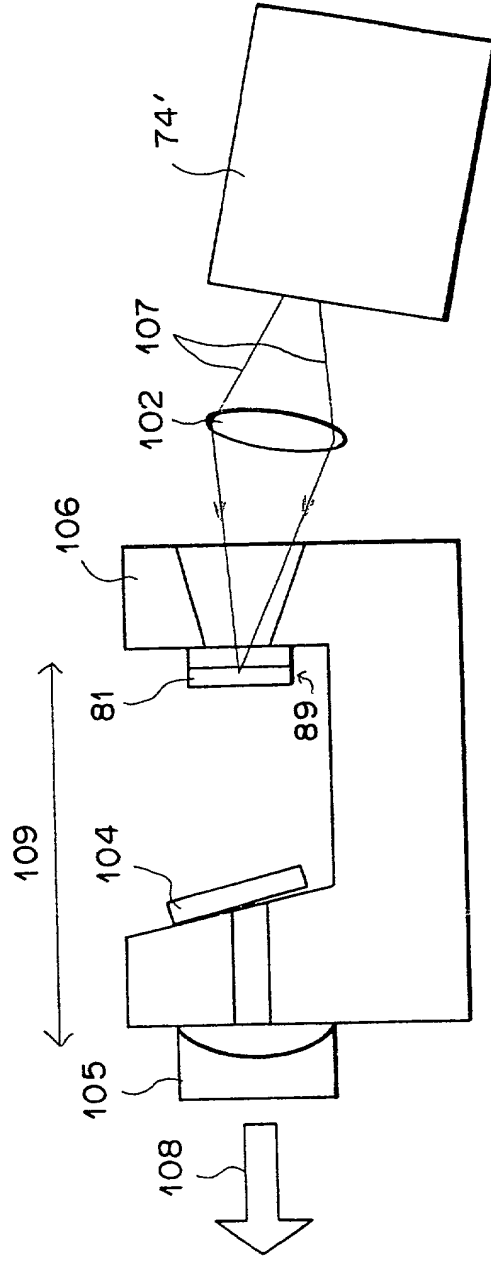
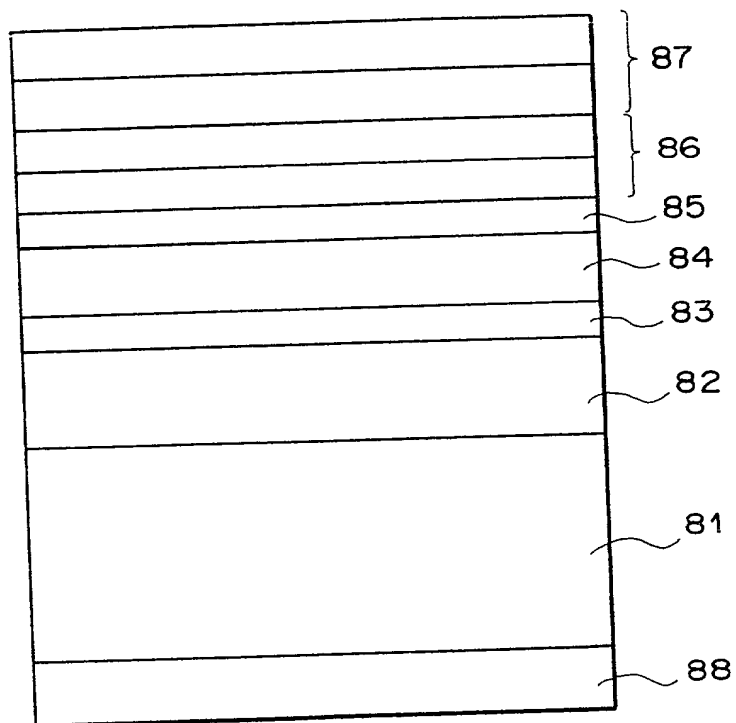


FIG. 8B

F I G . 9

89



F I G . 10

